Attorney Docket No.: SON-2624 Application No.: 10/505,431

## REMARKS

This Preliminary Amendment is requested prior to the initial examination of the above-identified patent application to correct minor grammatical errors. No new matter has been added.

If the Examiner has any suggestions for placing this application in even better form, the Examiner is invited to telephone the undersigned and the number listed below.

Dated: July 5, 2006

Respectfull submitte

Ronald P. Kananen

Registration No.: 24,104

RADER, FISHMAN & GRAUER PLLC

1233 20th Street, N.W., Suite 501

Washington, DC 20036

(202) 955-3750

Attorney for Applicant

Attorney Docket No.: SON-2624 Application No.: 10/505,431

## Marked-up Version Abstract of the Disclosure

Please amend the abstract as followed:

It is an object to provide, in a data storage circuit for storing data, provide a power saving data storage circuit and a data writing method in the data storage circuit, and, further, to provide a data storage device.

10 Thus, in the present invention, reading out efexisting data stored in a storage element M is performed prior to performing writing of new data to the storage element M to compare the existing data and the new data. The data storage circuit is configured so that in a case where the existing data and the new data are identical with each other, writing to the storage element M is not performed, and, in a case where the existing data and the new data are not identical with each other, writing of the new data to the storage element M is performed. The data storage circuit is formed on a semiconductor substrate to have a data storage device.